Fast GPU-based Adaptive Tessellation with CUDA
— Supplementary Material —

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In Fig. 1 we illustrate how the single stages of the CudaTess framework are organized into CUDA kernels in our implementation for bicubic rational Bézier patches. Moreover, all involved buffers and data flows are shown.
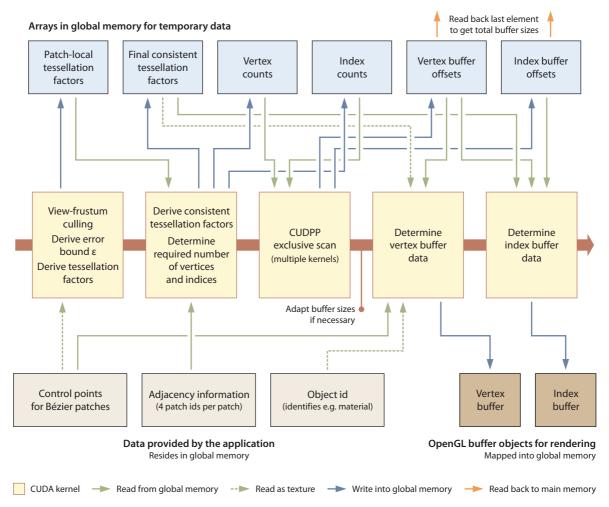

Figure 1: *Realization of the CudaTess framework for bicubic rational Bézier patches.*